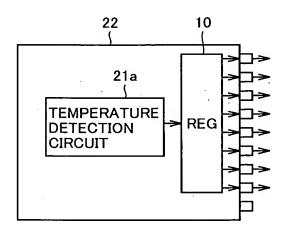
INVENTOR(S): TATSUO SENGOKU ET AL.

ATTORNEY'S DOCKET NO: 009683-488 SHEET 1 of 15

FIG.1

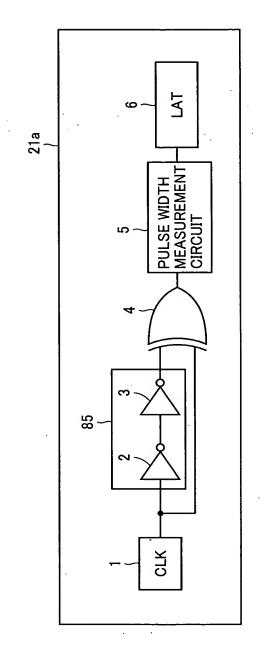


TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT

INVENTOR(S): TATSUO SENGOKU ET AL.

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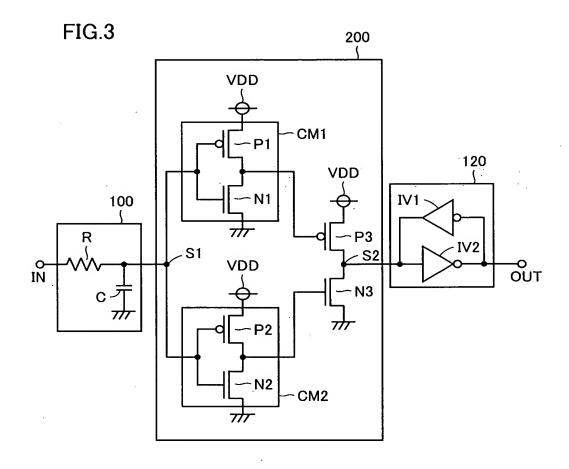
SHEET 2 of 15



:IG:2

INVENTOR(S): TATSUO SENGOKU ET AL. ATTORNEY'S DOCKET NO: 009683-488

SHEET 3 of 15



TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT

INVENTOR(S): TATSUO SENGOKU ET AL.

ATTORNEY'S DOCKET NO: 009683-488

SHEET 4 of 15

FIG.4A

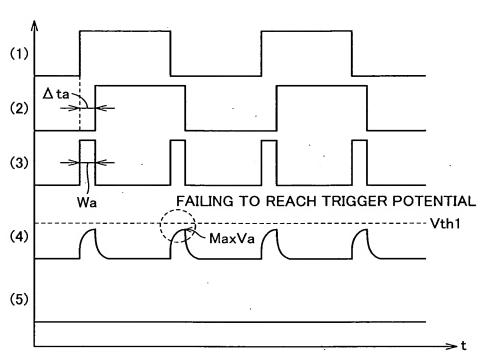
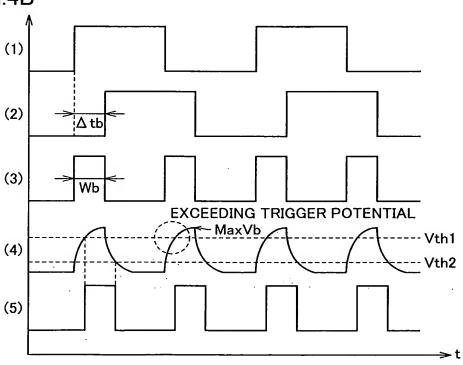
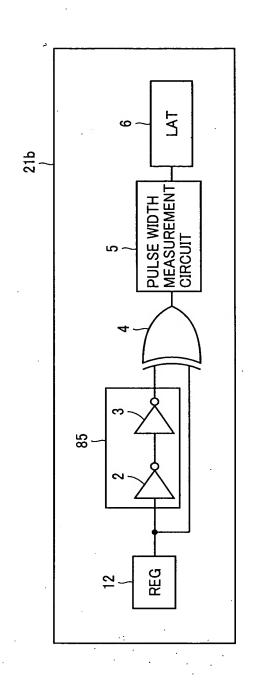


FIG.4B



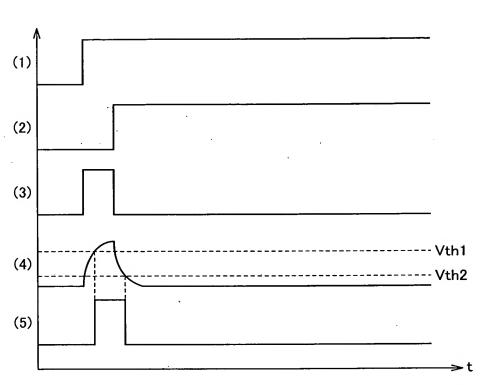
APPLN. FILING DATE: DECEMBER 9, 2003
TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT
INVENTOR(S): TATSUO SENGOKU ET AL.
ATTORNEY'S DOCKET NO: 009683-488 SHEET 5 o

SHEET 5 of 15



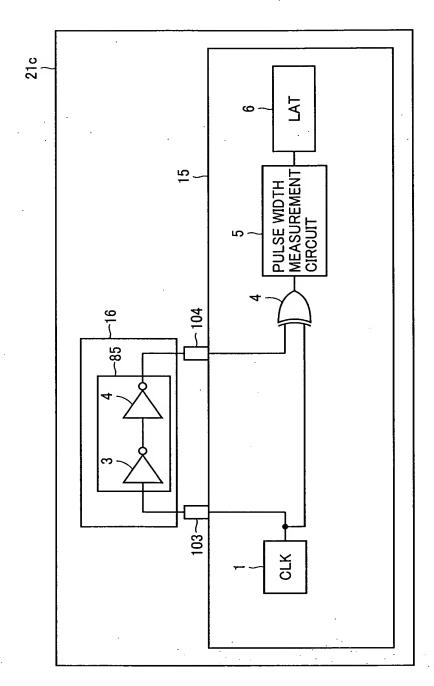
INVENTOR(S): TATSUO SENGOKU ET AL. ATTORNEY'S DOCKET NO: 009683-488 SHEET 6 of 15

FIG.6



APPLN. FILING DATE: DECEMBER 9, 2003
TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT
INVENTOR(S): TATSUO SENGOKU ET AL.
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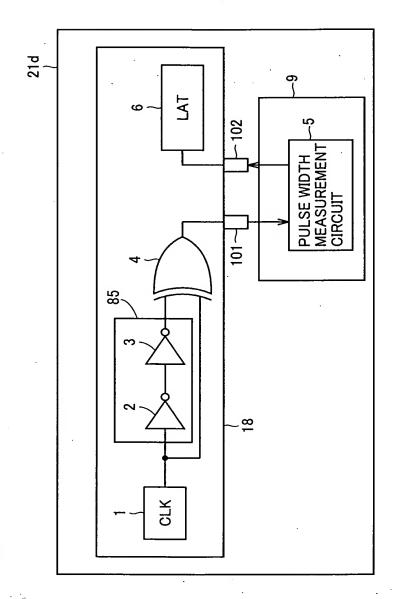
SHEET 7 of 15



TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT

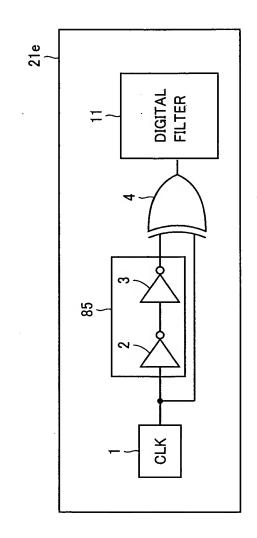
INVENTOR(S): TATSUO SENGOKU ET AL. ATTORNEY'S DOCKET NO: 009683-488

SHEET 8 of 15



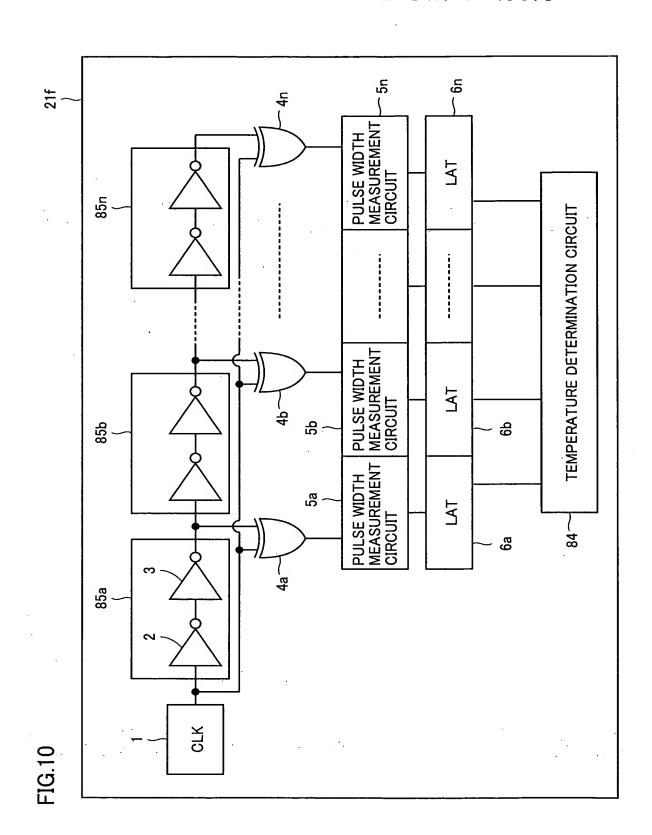
APPLN. FILING DATE: DECEMBER 9, 2003
TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT
INVENTOR(S): TATSUO SENGOKU ET AL.
ATTORNEY'S DOCKET NO: 009683-488 SHEET 9 of

SHEET 9 of 15



INVENTOR(S): TATSUO SENGOKU ET AL. ATTORNEY'S DOCKET NO: 009683-488

SHEET 10 of 15



APPLN. FILING DATE: DECEMBER 9, 2003
TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT
INVENTOR(S): TATSUO SENGOKU ET AL.

ATTORNEY'S DOCKET NO: 009683-488 SHEET 11 of 15

FIG.11A

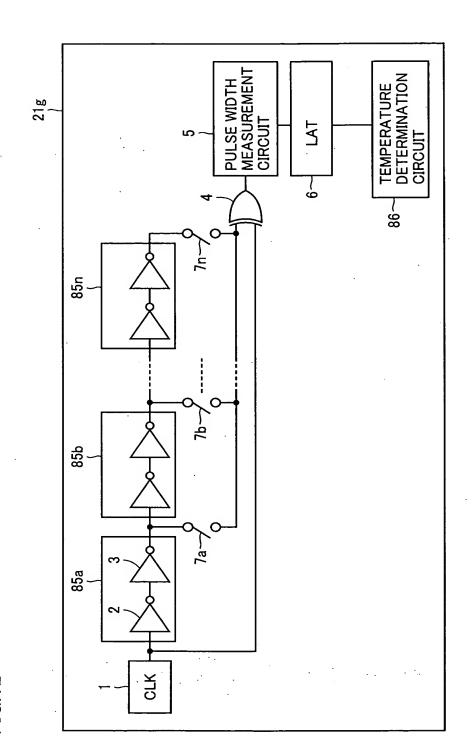
LATCH CIRCUIT	VALUE OF SIGNAL HELD
LATCH CIRCUIT6a	L
LATCH CIRCUIT6b	L.
LATCH CIRCUIT6c	L
LATCH CIRCUIT6d	· · H
•	•
-	•
•	•
LATCH CIRCUIT6m	Н
LATCH CIRCUIT6n	Н

FIG.11B

LATCH CIRCUIT	VALUE OF SIGNAL HELD
LATCH CIRCUIT6a	L
LATCH CIRCUIT6b	Н
LATCH CIRCUIT6c	Н
LATCH CIRCUIT6d	Н
•	•
•	•
•	•
LATCH CIRCUIT6m	Н
LATCH CIRCUIT6n	Н

TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT INVENTOR(S): TATSUO SENGOKU ET AL.
ATTORNEY'S DOCKET NO: 009683-488 SHEET 12 o.

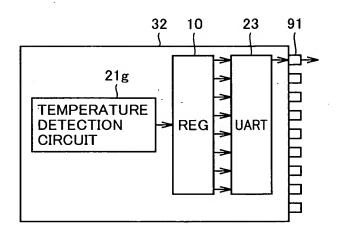
SHEET 12 of 15



TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT

INVENTOR(S): TATSUO SENGOKU ET AL. ATTORNEY'S DOCKET NO: 009683-488 **SHEET 13 of 15**

FIG.13



TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT

INVENTOR(S): TATSUO SENGOKU ET AL.

ATTORNEY'S DOCKET NO: 009683-488 SHEET 14 of 15

FIG.14

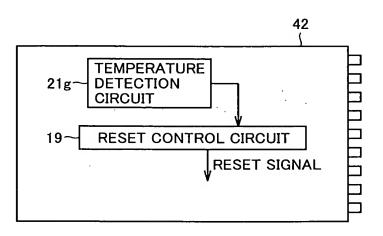
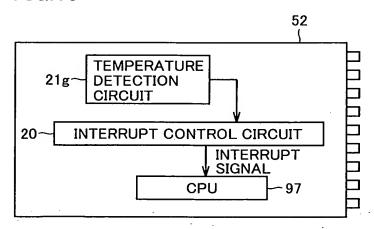


FIG.15



APPLN. FILING DATE: DECEMBER 9, 2003 TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT

INVENTOR(S): TATSUO SENGOKU ET AL.

ATTORNEY'S DOCKET NO: 009683-488 **SHEET 15 of 15**

FIG.16

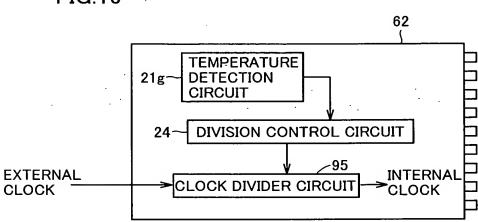


FIG.17 72 TEMPERATURE **DETECTION** 21g-**CIRCUIT** VDC CONTROL CIRCUIT **EXTERNAL** INTERNAL POWER SUPPLY > POWER SUPPLY **VDC CIRCUIT** POTENTIAL **POTENTIAL**